

Customer No. 24498
Serial No.: 10/563,432

PD030073

Remarks

In view of the following discussion, the applicants submit that the claims now pending in the application are not anticipated under the provisions of 35 U. S. C. § 102. Thus, the applicants believe that all of these claims are in allowable form.

Amendments to claims

Amended claim 1 has been better defined to further distinguish the present invention. Amended claim 1 now recites "a control circuit with a driver stage coupled to a control input of the switching transistor for controlling an output voltage, the control circuit comprising an oscillator with a terminal, to which a first capacitor is coupled and the control circuit providing a reference voltage for charging said capacitor for determining the oscillation frequency of said oscillator for a start-up phase", Support for this amendment can be found in figures 1 and 2, Vref at pin 9, and the respective parts of the description. The voltage Vref provides a current for charging the capacitor, for determining the oscillation frequency of the oscillator "for a start-up phase", as disclosed on page 5, lines 9-15 and page 8, lines 2-27 of the specification.

Amended claim 1 also recites "a start-up circuit coupled to a second capacitor for providing an operating voltage for the control circuit,". Support for this can be found on on page 9, lines 30-34, also in figures 1 and 2 wherein the start-up circuit is coupled to a second capacitor (C3) for providing an operating voltage for the control circuit.

The further feature: "a third rectifier means coupled between said first rectifier means and the second capacitor for charging the second capacitor during operation of the switched mode power supply" was disclosed in claim 8 as

Customer No. 24498
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PD030073

well as in figure 2 and page 7, lines 34-38. Claim 8 has been deleted correspondingly. The expression "node between the first rectifier means and third rectifier means" is disclosed in figure 2.

Some minor changes have been made also for the new claim 5.

REJECTIONS

A. 35 U. S. C. § 102

1. Claims 1, 3-5 and 7-9 are not anticipated by ON Semiconductor "High Performance Current Mode Controller" data sheet

Claims 1, 3-5 and 7-9 stand rejected under 35 U. S. C. § 102(b) as being anticipated by ON Semiconductor "High Performance Current Mode Controller" data sheet (ON Semiconductor "High Performance Current Mode Controller" data sheet published September 2004). The applicants submit that these claims are not anticipated by this reference.

The switched mode power supply according to amended claim 1 comprises a transformer with a primary winding and at least one secondary winding, a switching transistor arranged in series with the primary winding and a control circuit coupled with a driver stage to a control input of the switching transistor for controlling an output voltage. The control circuit comprises an oscillator having a terminal, to which a first capacitor is coupled for determining the oscillation frequency of the oscillator for a start-up phase. A start-up circuit is coupled to a second capacitor for providing an operating voltage for the control circuit, and a third rectifier means is coupled between said first rectifier means and the second capacitor for charging the second capacitor during operation of the switched mode power supply. The terminal is further coupled via a resistor to a node between the first rectifier means and the third rectifier means, for

Customer No. 24498
Serial No.: 10/563,432

PD030073

providing an additional charge current for said capacitor for increasing the oscillation frequency of the oscillator during operation, with regard to the start-up phase.

The prior art reference does not comprise a resistor coupled between the first capacitor and a node between a first and a third rectifier means for providing an additional charge current for increasing the oscillation frequency of the oscillator during operation, with regard to the start-up phase. The switched mode power supply as disclosed in figure 16 of the prior art reference, also as shown in the other figures, operates only with a constant oscillation frequency defined by the values of resistor R_T , capacitor C_T and of the voltage reference V_{ref} . No additional charge current is provided for the capacitor C_T for increasing the oscillation frequency of the oscillator during operation, with regard to the start-up phase. Amended claim 1 is therefore new and inventive over the cited prior art reference.

Claims 3-5 and 7-9 depend directly, or indirectly, from claim 1. For the same reasons as stated above for claim 1, claims 3-5 and 7-9 are also patentable over cited data sheet for the ON Semiconductor "High Performance Current Mode Controller"

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CONCLUSION


Thus, the applicants submit that none of the claims presently in the application are anticipated under the provisions of 35 U. S. C. § 102. Consequently, the applicants believe that all of the claims are presently in condition for allowance. Accordingly, both reconsideration of this application and its swift passage to issue are earnestly solicited.

If, however, the Examiner believes that there are any unresolved issues requiring adverse final action in any of the claims now pending in the application, it is requested that the Examiner telephone James McKenzie, at (609) 734-6866, so that appropriate arrangements can be made for resolving such issues as expeditiously as possible.

Please charge the \$490.00 fee for the Two-Month Petition for Extension of Time, and any other cost that may be due, to Deposit Account No. 07-0832.

Respectfully submitted,

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